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PATENT 10/8/02
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09/164,216

Response to Office Action Mailed June 25, 2002

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Ronald Pasqualini

Appln. No.: 09/164,216

Filed: September 30, 1998

For: ESD PROTECTION CIRCUIT UTILIZING
FLOATING LATERAL CLAMP DIODES

Group Art Unit: 2811

Examiner: O. Nadav

RESPONSE TO OFFICE ACTION MAILED
JUNE 25, 2002

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the final rejection mailed on June 25, 2002, please amend the above-identified application as follows:

In the Claims

The following claims have been amended to read as follows:

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- G' 15. (Fifth Amendment) A semiconductor chip having a substrate of a first conductivity type, the chip comprising:
- a plurality of pads;
 - an electrostatic discharge (ESD) negative ring;
 - a plurality of ESD positive lines, the plurality of positive lines not being electrically connected to each other, none of the positive lines being directly connected to a pad;
 - a plurality of ESD switches connected to the ESD positive lines and the ESD negative ring so that each positive line is connected to the negative ring via an ESD switch;
 - a plurality of first diodes connected to the pads so that each first diode is connected to a pad and the negative ring; and